



CHIPS JOINT UNDERTAKING

Jari Kinaret

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EUROPEAN
PARTNERSHIP



WHAT IS CHIPS JOINT UNDERTAKING?

Public-private partnership (PPP)

Partnerships between public authorities and industry intend to bring project results closer to the market and improve the link between research and societal growth. The PPPs are based on long term contracts that can take many different legal forms, from contractual partnerships to specific legal entities.

Joint undertaking (JU)

A Joint Undertaking is an institutionalized PPP with its own legal identity, with its own governance, budget etc.. In most cases, like for the Chips JU, the JUs are established by an EU regulation called the Single Basic Act which defines what the JU has to do and how it must do it. Most of the rules governing the JUs are very similar to those of the European Commission.

Chips JU

Chips JU was established in September, 2023, in an amendment to the Single Basic Act to implement the first pillar of the Chips Act and to continue the activities of its predecessors in the field of electronic components and systems (ECS). The Chips JU is a tri-partite partnership between the EC, the participating states and European industries; most of our actions are funded jointly and equally by these actors.

CHIPS ACT: ENTRY INTO FORCE, 21 SEPTEMBER 2023

SIGNATURES 13 SEPTEMBER, PUBLICATION 18 SEPTEMBER 2023



Roberta Metsola (European Parliament President)

José Manuel Albares Bueno (Council Presidency)

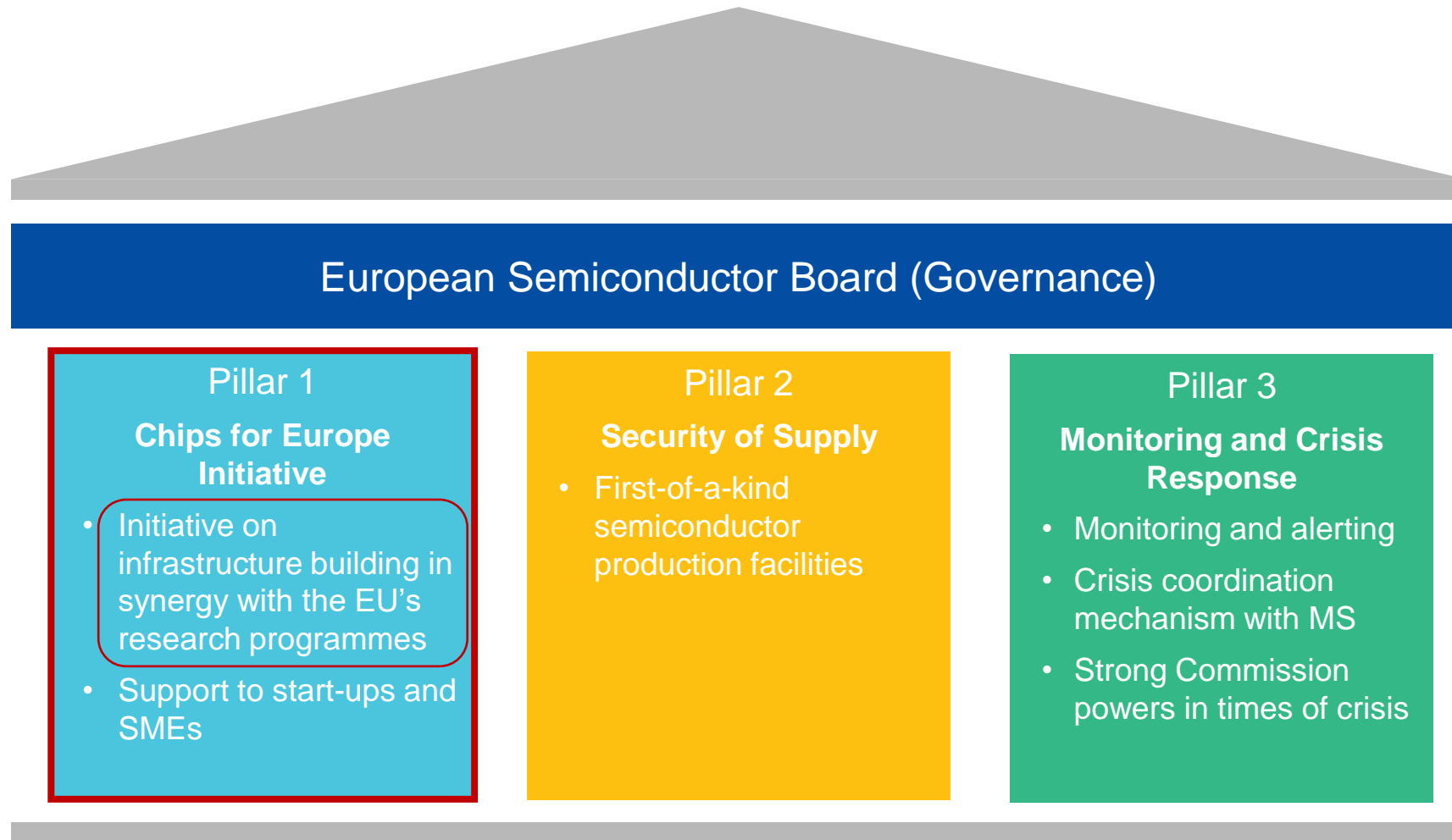
Chips Act:

<https://eur-lex.europa.eu/eli/reg/2023/1781/oj>

Single Basic Act amendment:

<https://eur-lex.europa.eu/eli/reg/2023/1782/oj>

THE 3 PILLARS OF THE CHIPS ACT



CHIPS JU AND ITS PREDECESSOR

KEY DIGITAL TECHNOLOGIES JU (KDT JU)

Non-initiative

- **KDT General Objectives**

- a) Reinforce EU strategic autonomy in electronic components and systems
- b) Establish EU scientific excellence and innovation leadership
- c) Ensure that components and systems technologies address Europe's societal and environmental challenges

Initiative

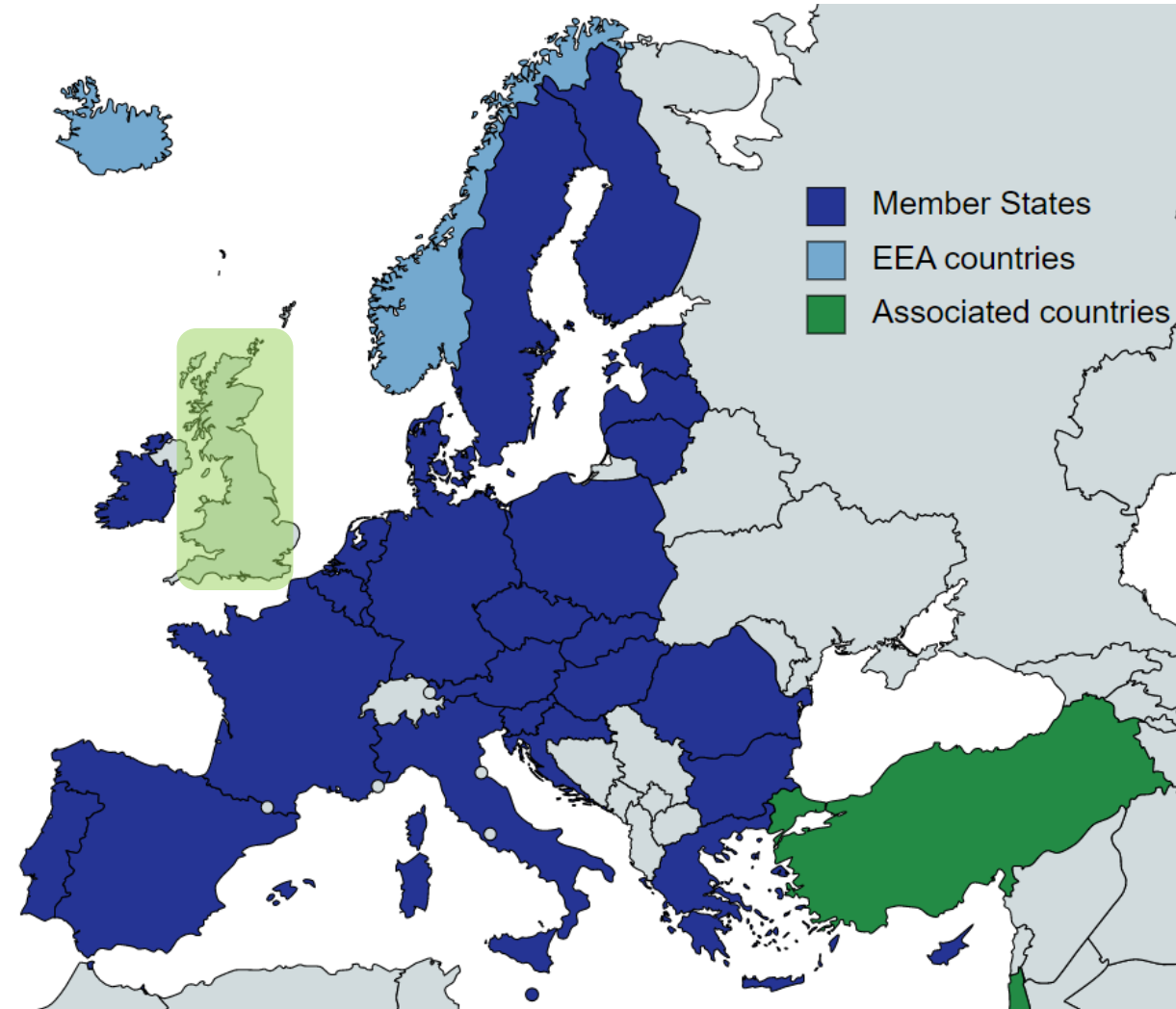
- **From KDT to Chips JU**

- d) Pilot lines
- e) Design platform
- f) Competence centers
- g) Quantum chips technology

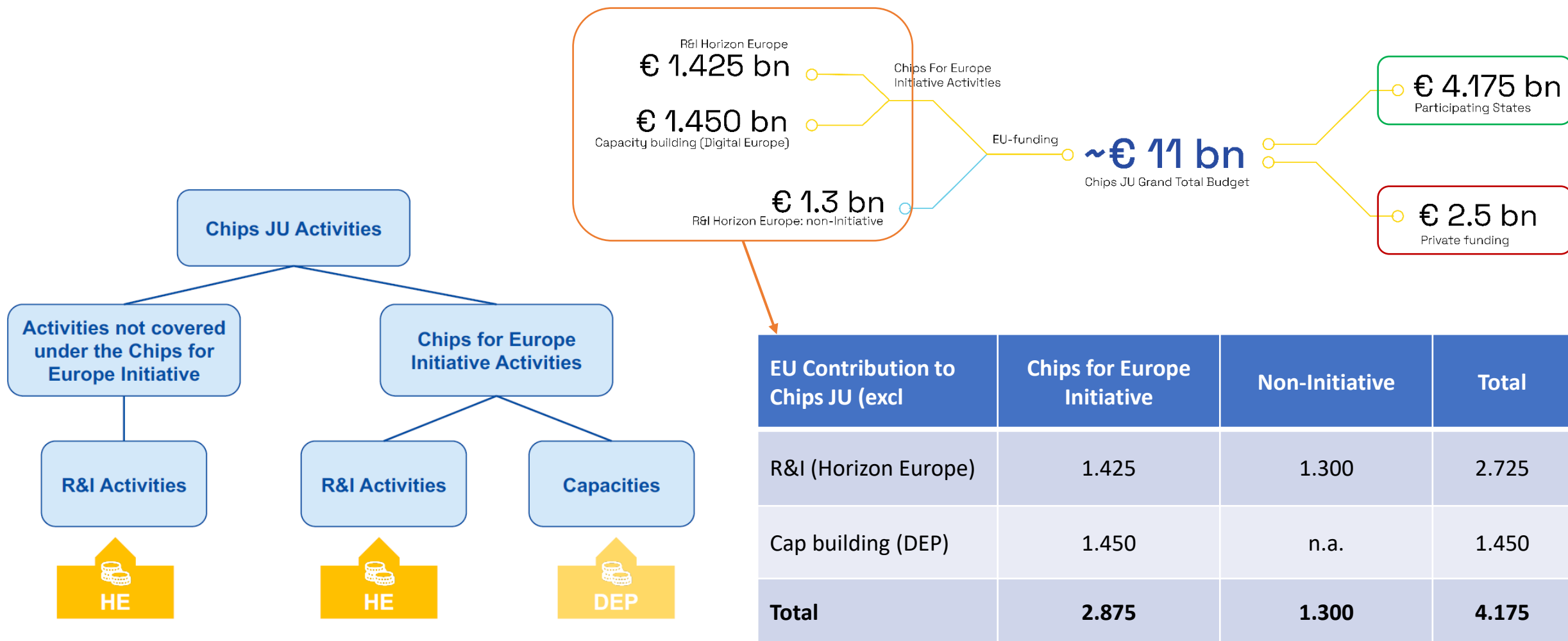
Digital Europe Programme in addition to Horizon Europe

- **Disclaimed: we know that the WP2023-2027 will need to be updated/amended in the spring and some details on the following pages may change**

- Link to work programme 2023-2027:
<https://www.chips-ju.europa.eu/Library-1> (may be updated!)



CHIPS JU



ITALY AND THE CHIPS JU



Italian partners in Chips/KDT projects with calls published in 2021-2023:

- Involved in 33 projects
- Altogether 178 entries of Italian beneficiaries
- Total project cost 124 M€ for the Italian partners
- Total EU funding 36.5 M€ for the Italian partners
- Most active partners
 - ST Microelectronics (11 projects)
 - Politecnico di Torino (10 projects)
 - U Bologna (9 projects)

In the 2022 Annual Activity Report, 10.5% of project beneficiaries were Italian, having 7.1% of project costs

CHIPS JU: FIRST CALLS OPENED IN DEC. 2023

Call	Topic	Max EU Contribution	Participating States' contribution	Total
Chips-CPL-1	Pilot line on advanced sub 2nm leading-edge system on chip technology	700 MEUR	700 MEUR	1,400 MEUR
Chips-CPL-2	Pilot line on advanced Fully Depleted Silicon On Insulator technologies targeting 7nm	420 MEUR	420 MEUR	840 MEUR
Chips-CPL-3	Pilot line on advanced Packaging and Heterogenous Integration	370 MEUR	370 MEUR	740 MEUR
Chips-CPL-4	Pilot line on advanced semiconductor devices based on Wide Bandgap materials	180 MEUR	180 MEUR	360 MEUR

- Calls closed on February 29, 2024. Complex call starting with a Call for Expression of Interest leading to a Hosting Agreement and a Joint Procurement Agreement, and calls for related HE and DEP grants.
- Expected project start in late 2024.



CHIPS JU: UPCOMING CALLS (NON-INITIATIVE)

Call	Topic	Max EU Contribution
Chips-2024-1-IA Topic 1	Innovation Action on major challenges identified in the Strategic Research and Innovation Agenda (SRIA)	103 MEUR
Chips-2024-1-IA Topic 2	High performance RISC-V automotive processors	20 MEUR
Chips-2024-1-IA Topic 3	Software-defined vehicle	20 MEUR
Chips-2024-2-RIA Topic 1	Research and Innovation Action on major challenges identified in the SRIA	52 MEUR
Chips-2024-2-RIA Topic 2	Sustainable and greener manufacturing	15 MEUR
Chips-2024-3-RIA	Joint call with Korea on heterogeneous integration and neuromorphic computing technologies	6 MEUR

Calls opened on Feb 6, project outlines are due on May 14, and full project proposals on Sept 17, except for the one-stage call with Korea where the deadline for a full proposal is May 14



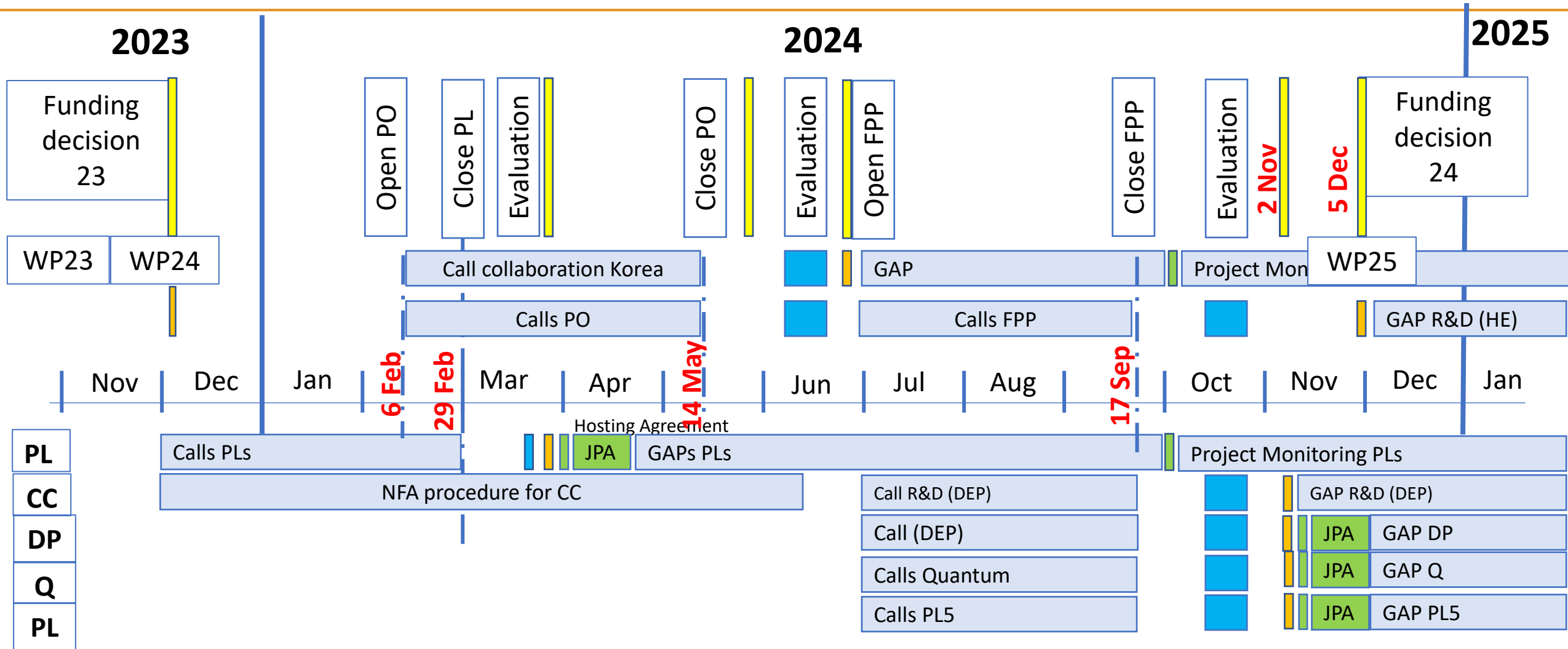
CHIPS JU: UPCOMING CALLS (INITIATIVE)

Call	Topic	Max EU Contribution
Chips-2024-CDP-1	Design platform. A cloud-based virtual platform that will enable users, particularly academia, start-ups and SMEs, to design and develop their chips. <i>Details forthcoming.</i>	330 MEUR
Chips-2024-CPL-5	Additional pilot line(s). <i>Details forthcoming.</i>	180 MEUR
Chips-2024-CQC-1	Quantum chips technology (preparatory action for a pilot line)	30 MEUR
Chips-2024-CCC-1	Competence centres. The centres are to provide access to technical expertise and experimentation in the area of semiconductors, helping companies, SMEs in particular, to approach and improve design capabilities and developing skills. Max 1 per EU MS or EEA country, funding up to 1 MEUR/yr from the EU for 4 years, to be matched nationally. Restricted call after national processes.	116 MEUR
Chips-2024-CCC-2	European Network of Chips Competence Centres (CSA)	4 MEUR

Calls will open July 8 and close Sept 17.



SUMMARY OF CALLS 2024



THANK YOU!

